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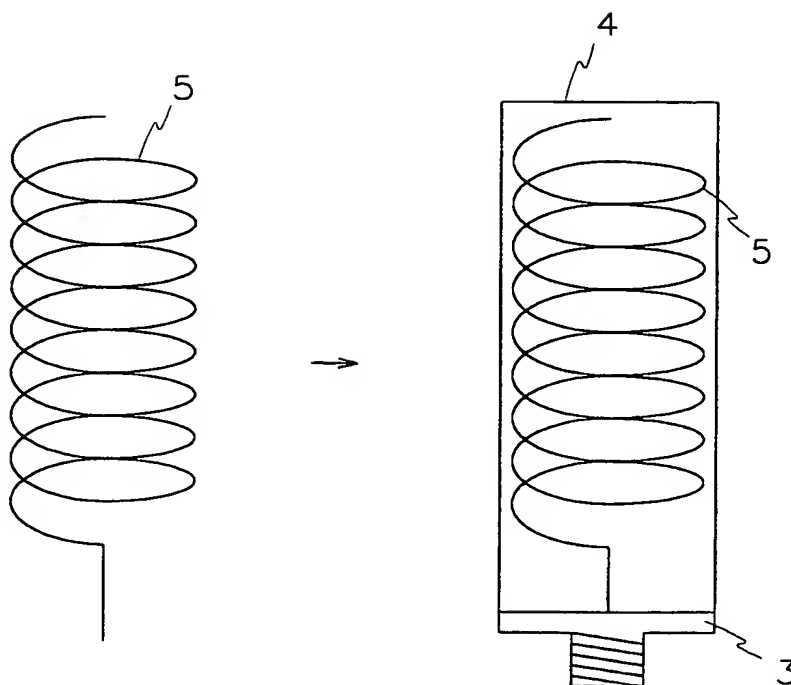
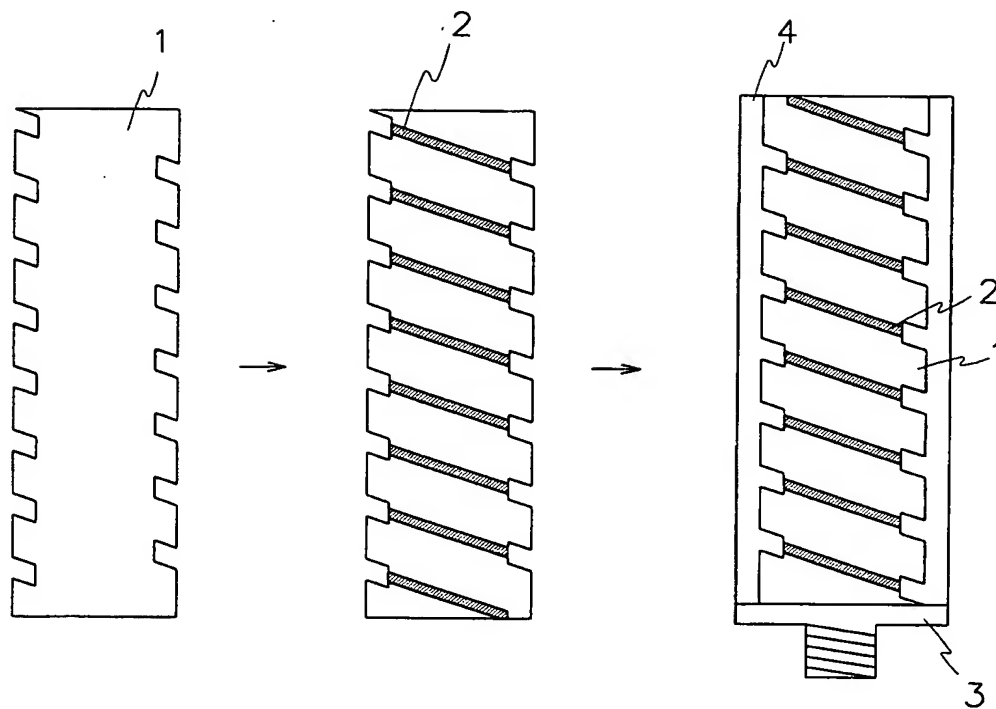
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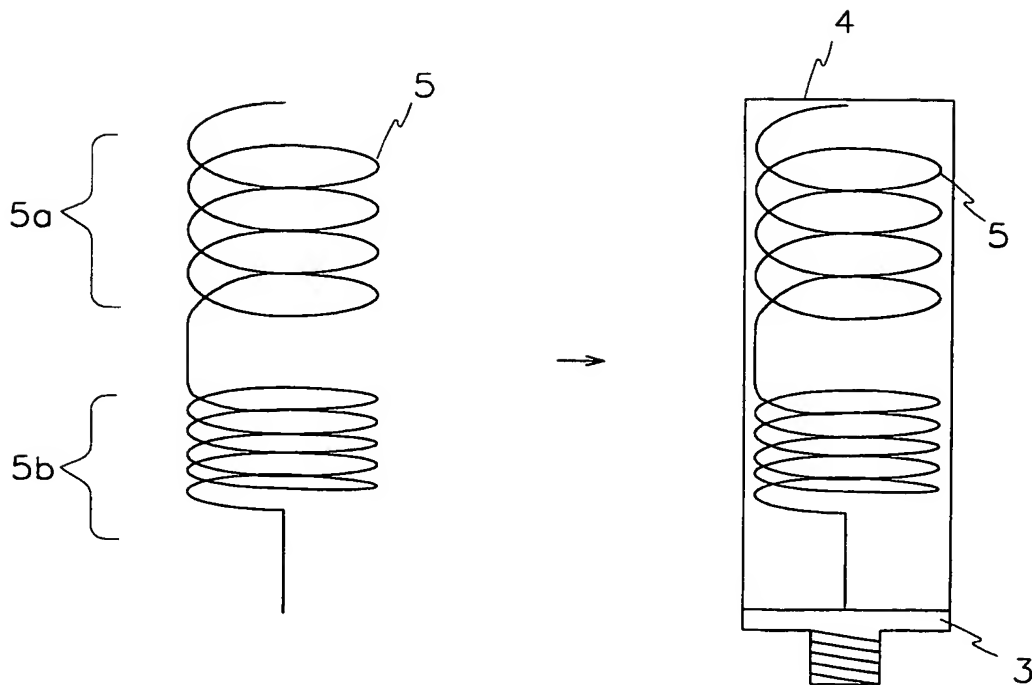
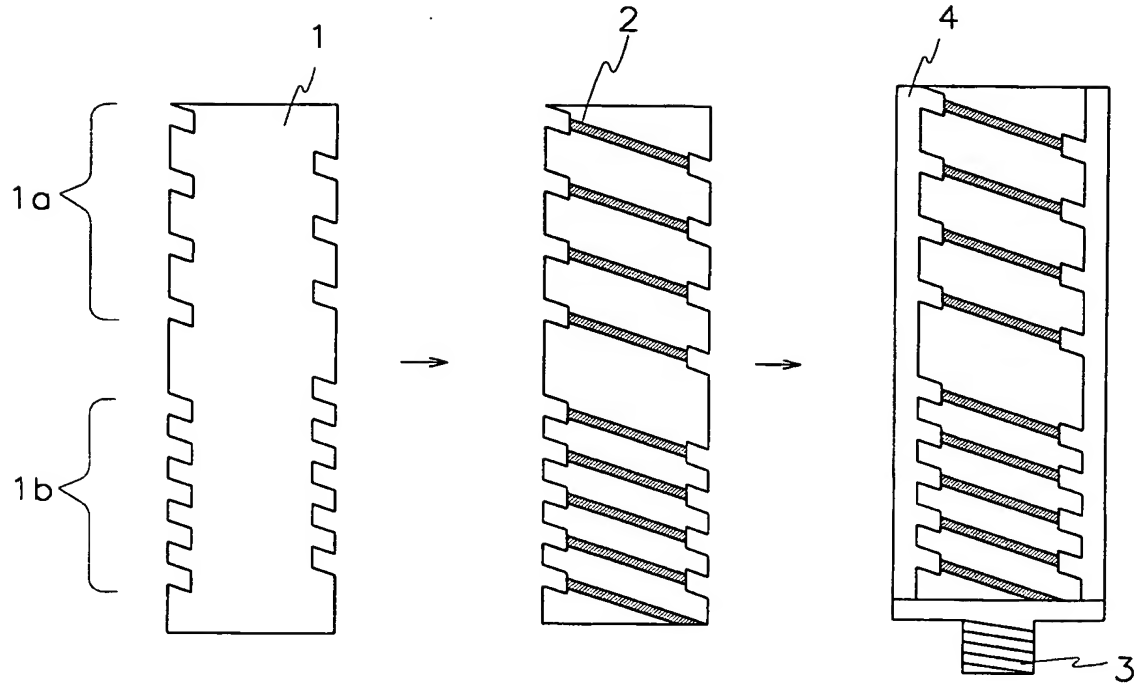
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FIG. 1

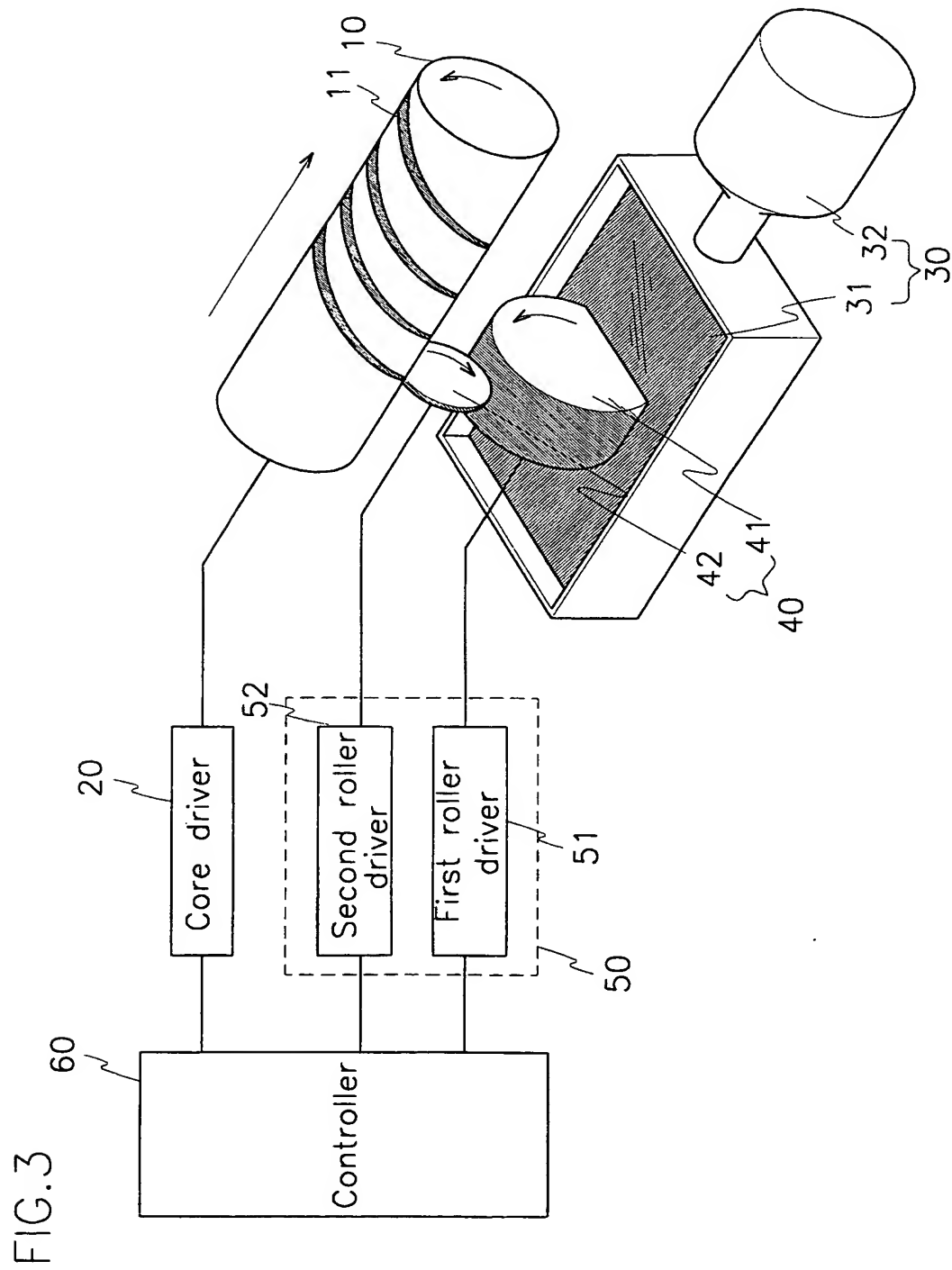


200070710 10009321

FIG. 2

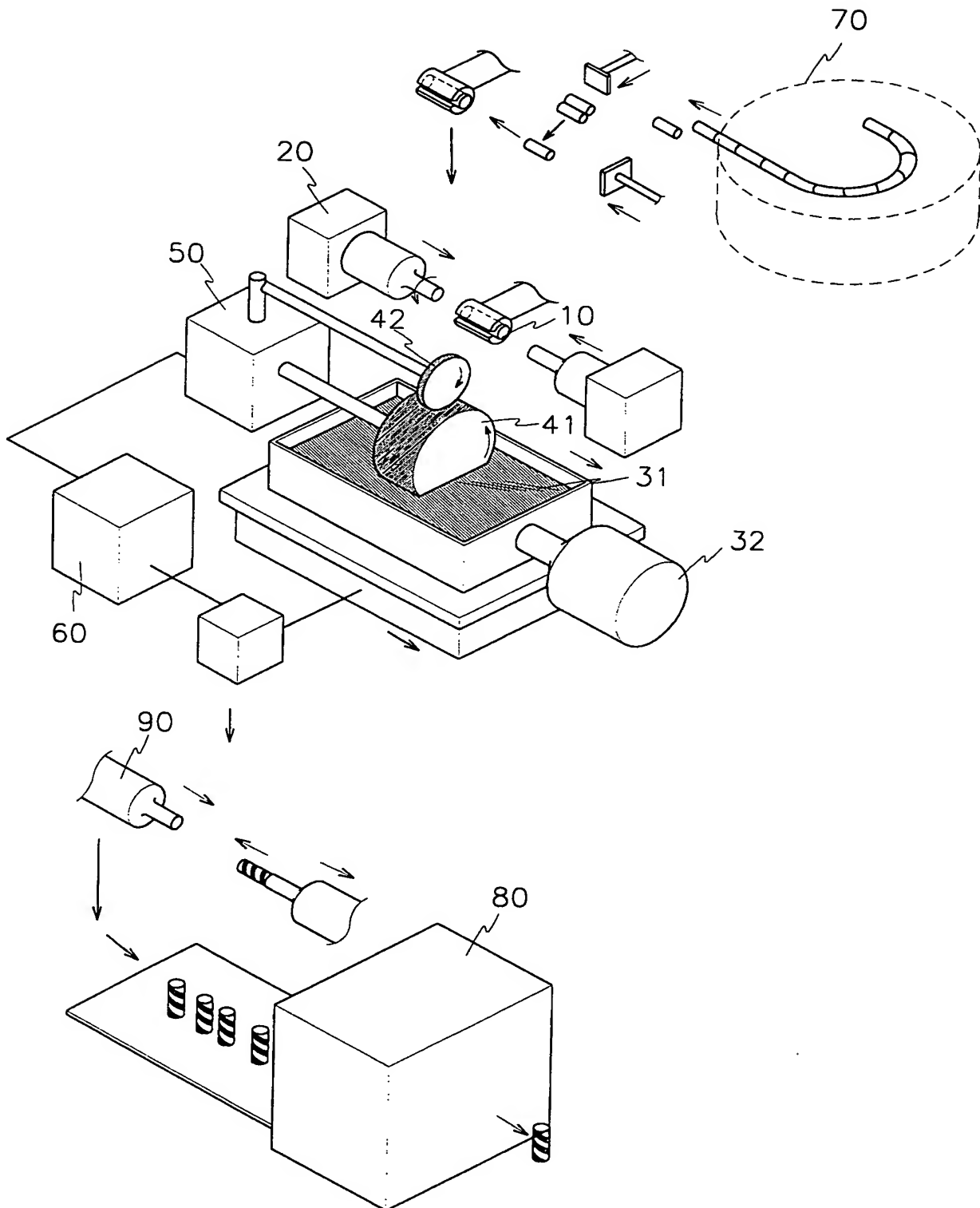


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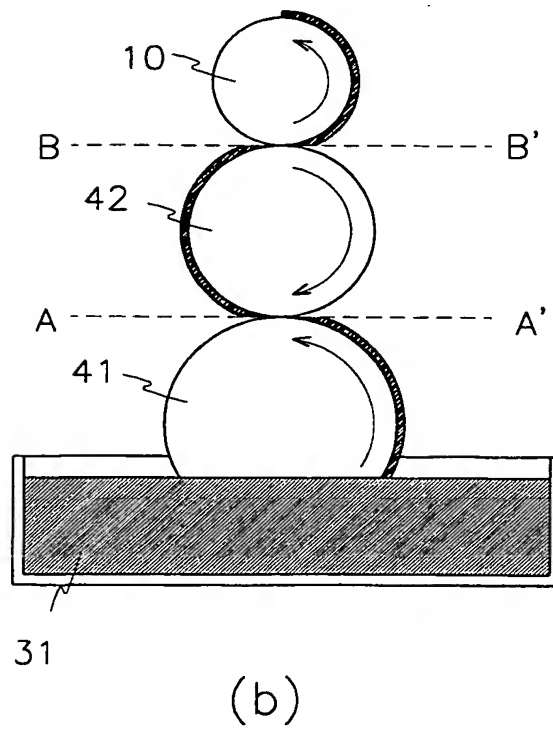
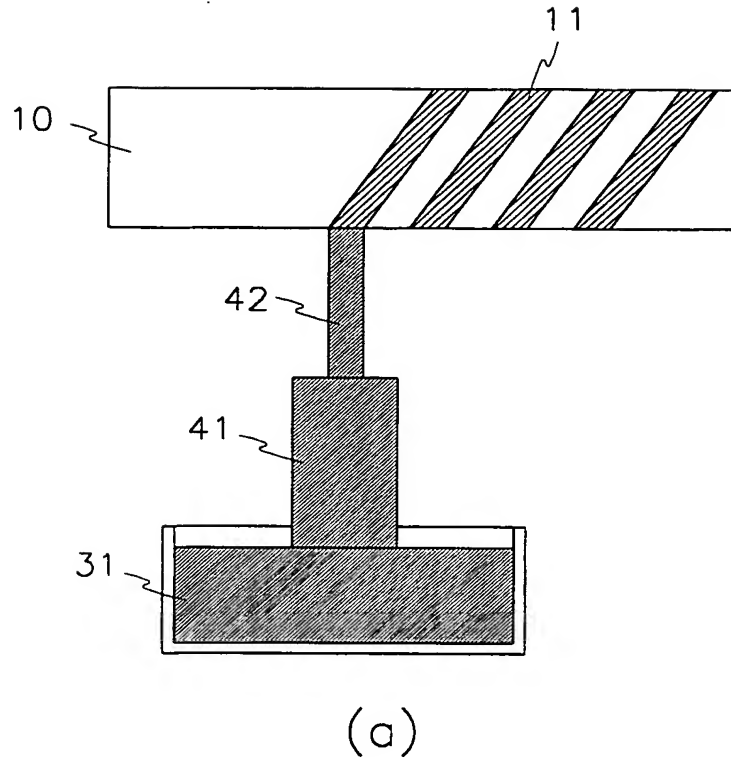
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FIG. 4



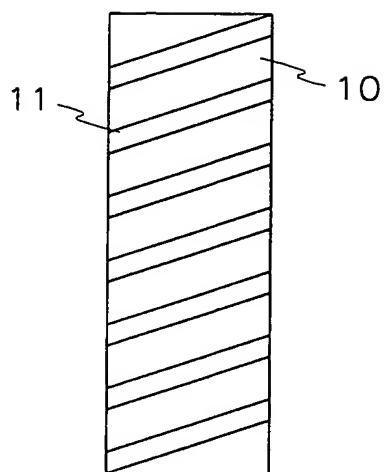
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FIG.5

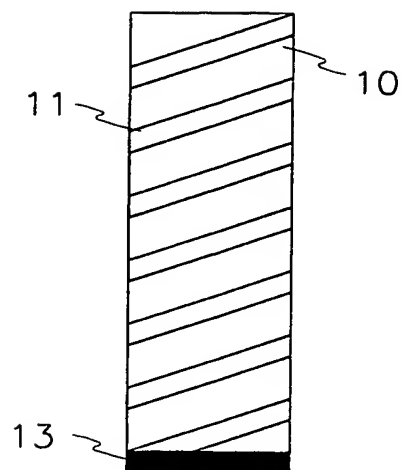


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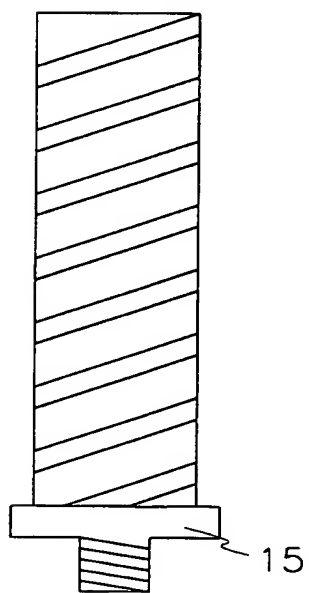
FIG.6



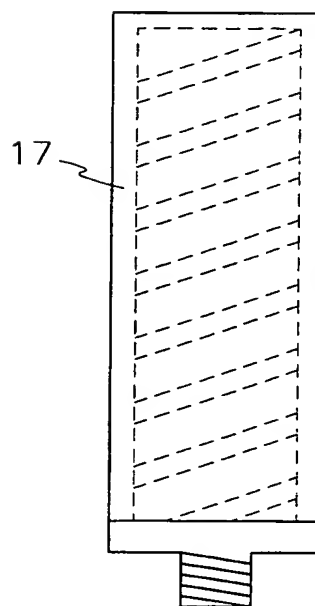
(a)  
(Helical coating)



(b)  
(dipping)



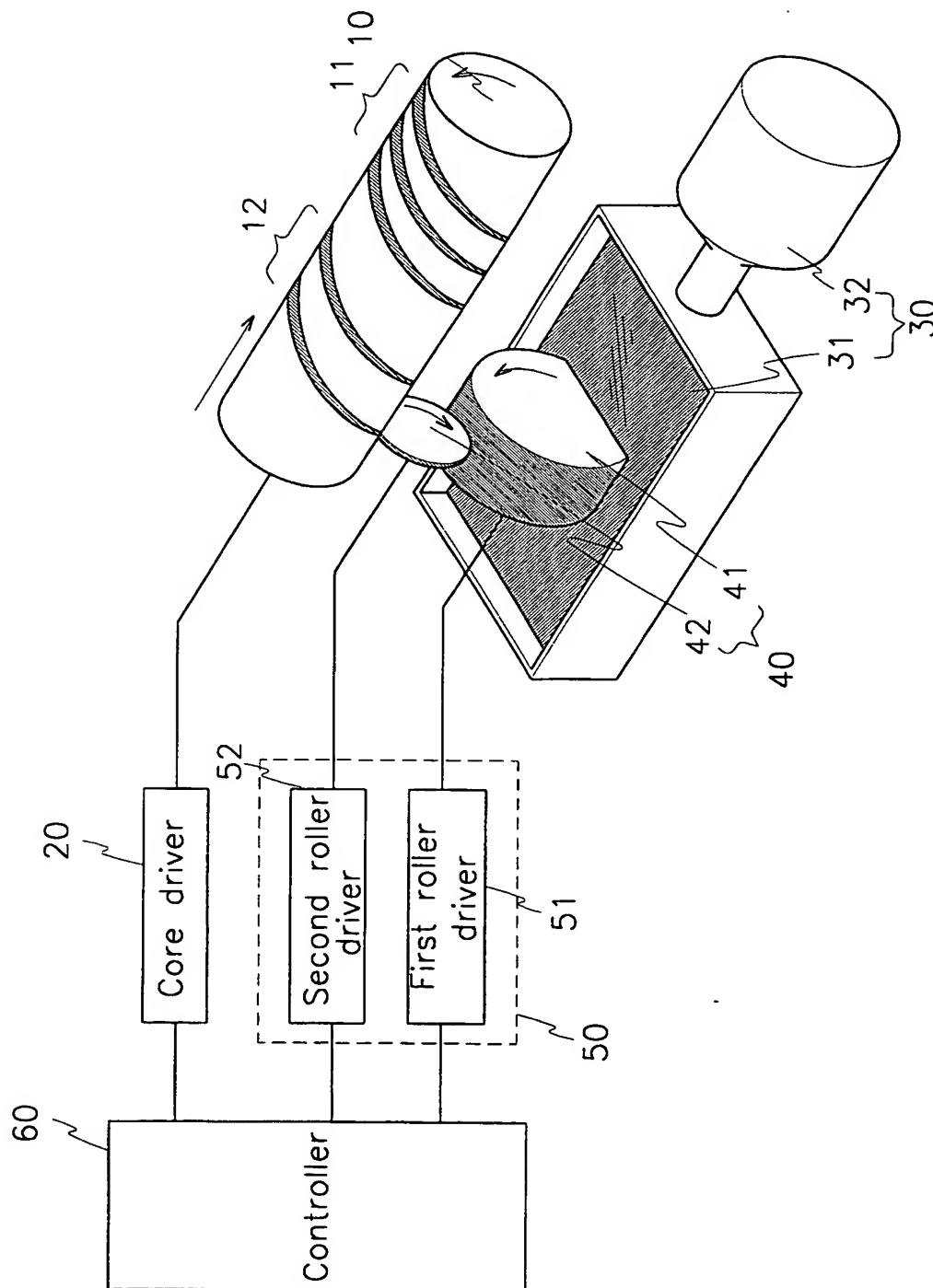
(c)  
(Soldering)



(d)  
(Molding)

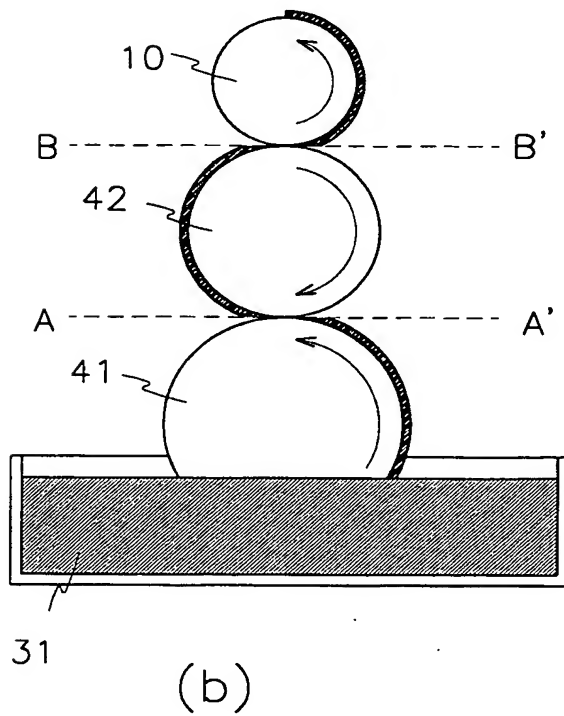
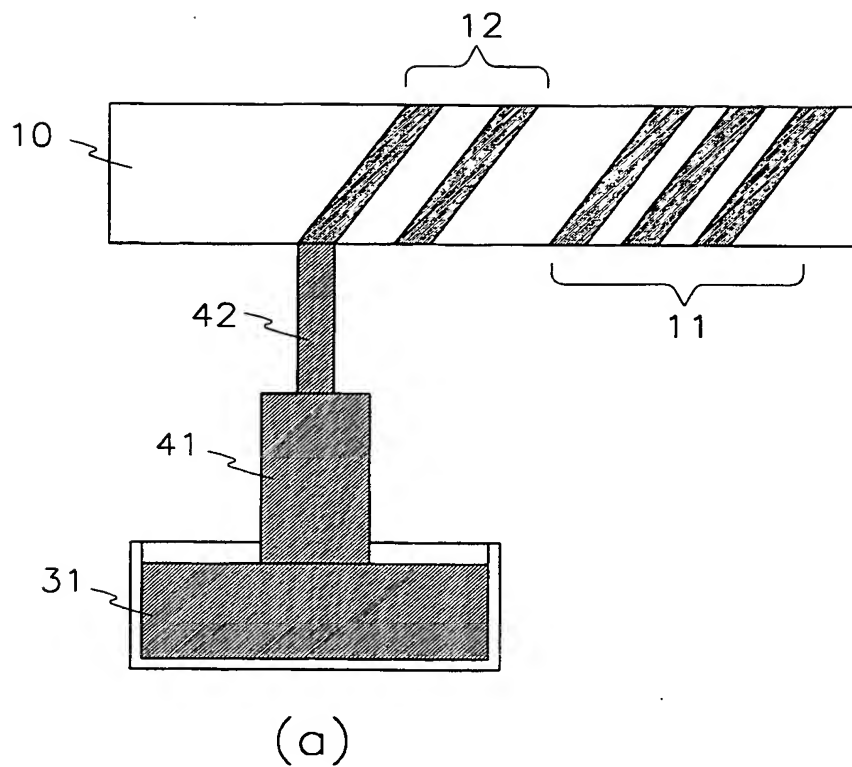
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FIG. 7



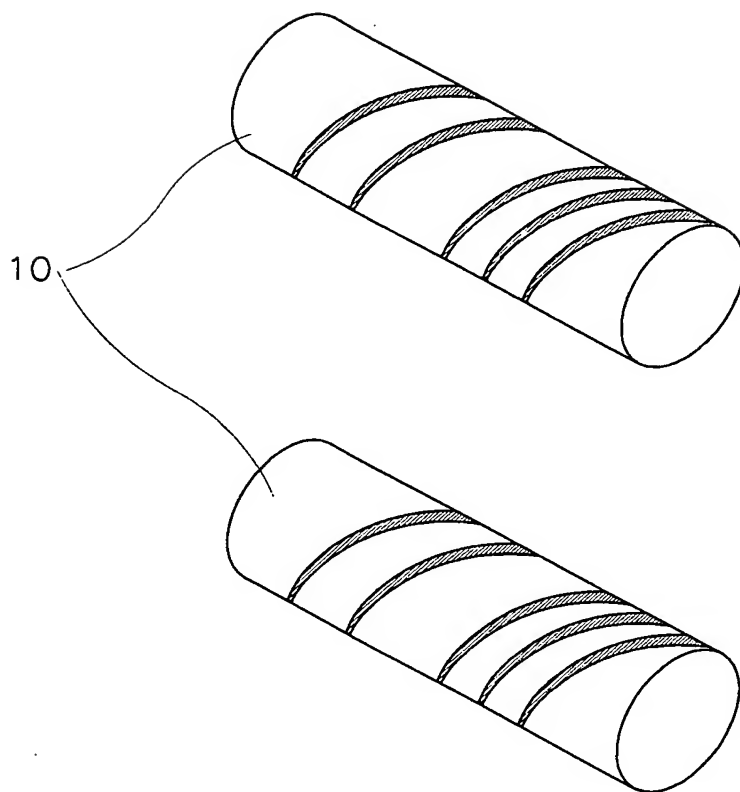
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FIG.8



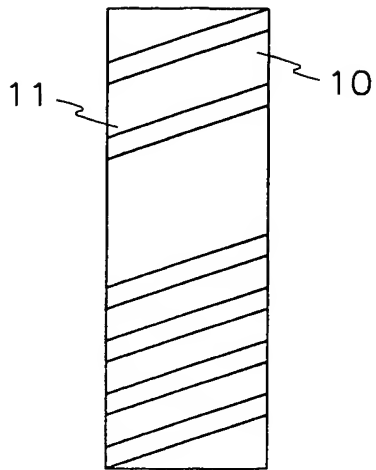
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FIG.9

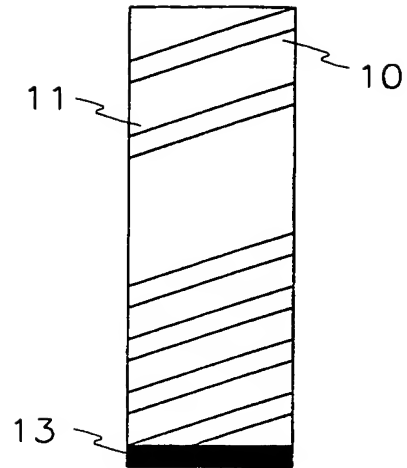


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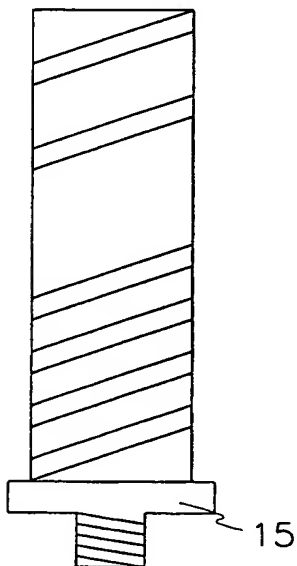
FIG. 10



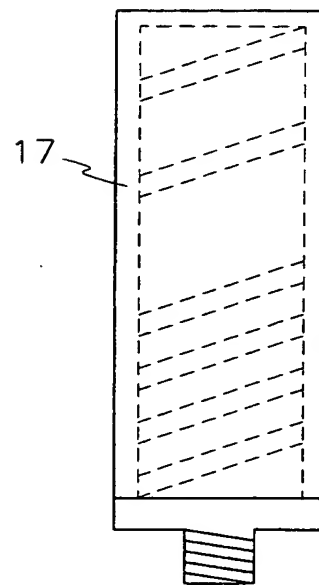
(a)  
(Helical coating)



(b)  
(dipping)



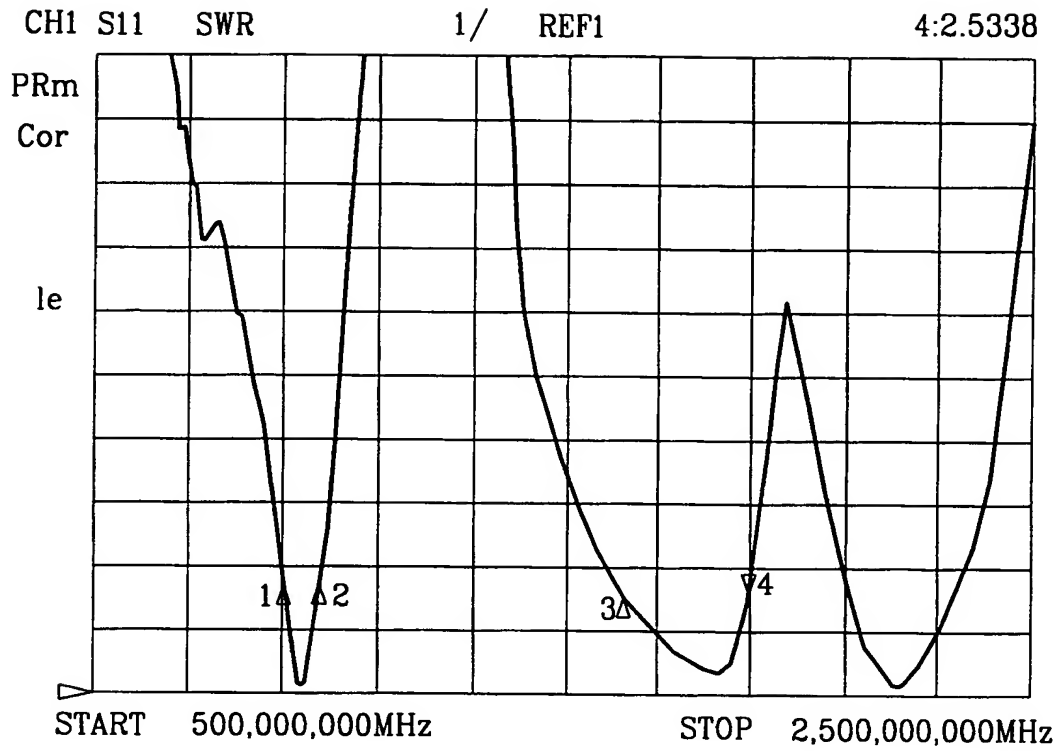
(c)  
(Soldering)



(d)  
(Molding)

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FIG. 1 1



f1 : 971MHz (75MHz)  
f2 : 1,768MHz (249MHz)

FIG. 12

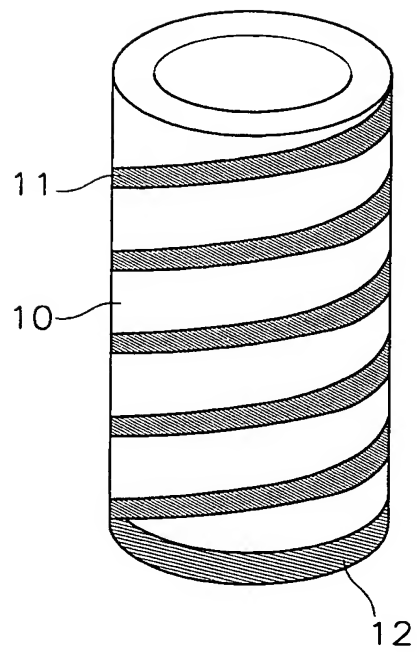
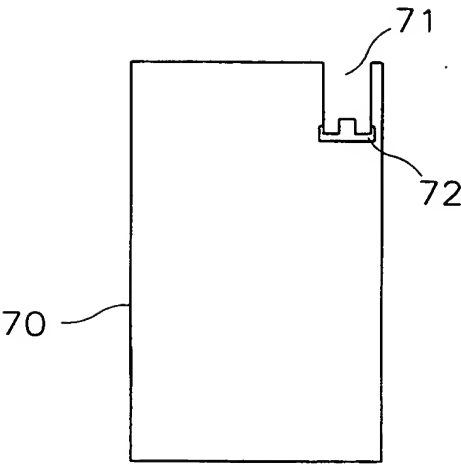
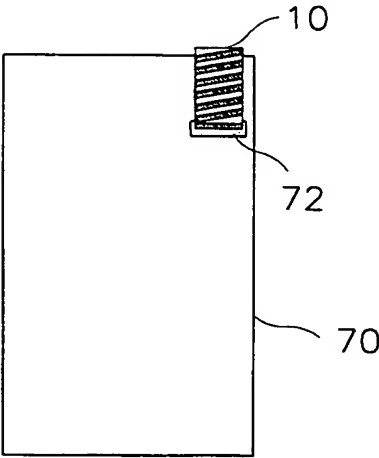


FIG. 13

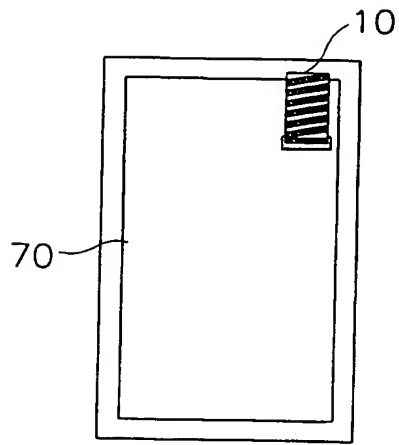


(a)

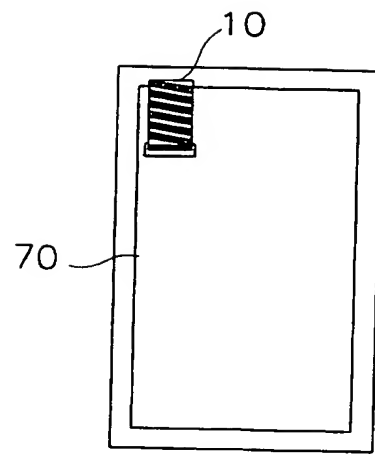


(b)

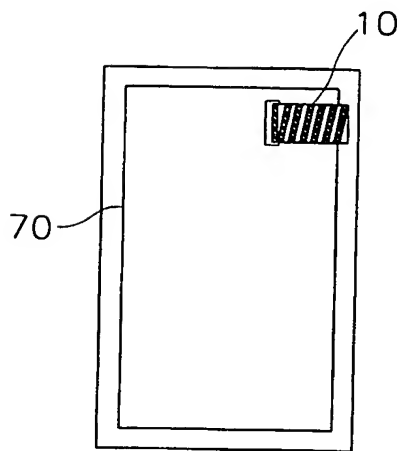
FIG. 14



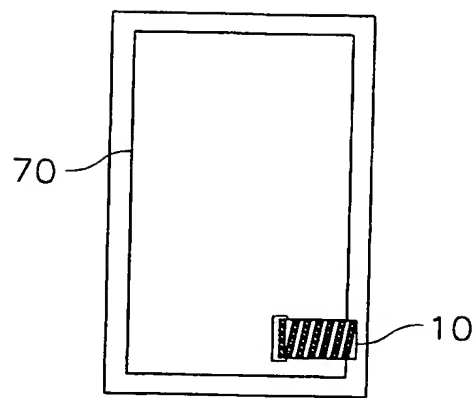
(a)



(b)

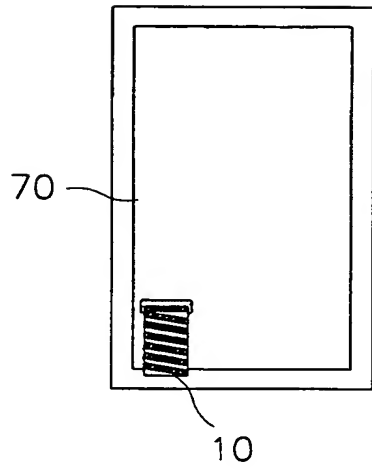


(c)

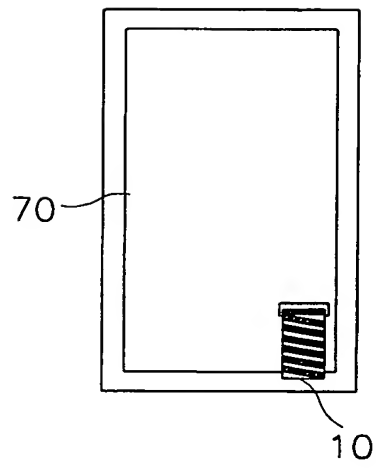


(d)

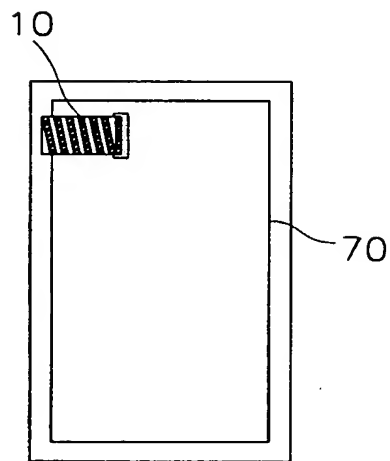
FIG.15



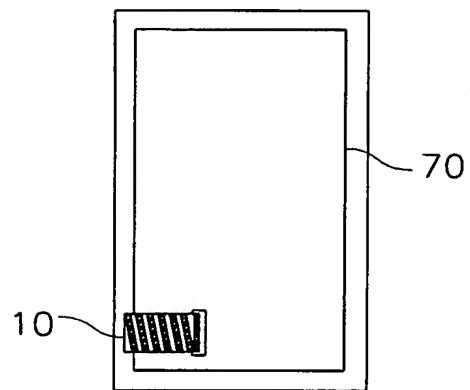
(a)



(b)



(c)



(d)

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FIG. 16

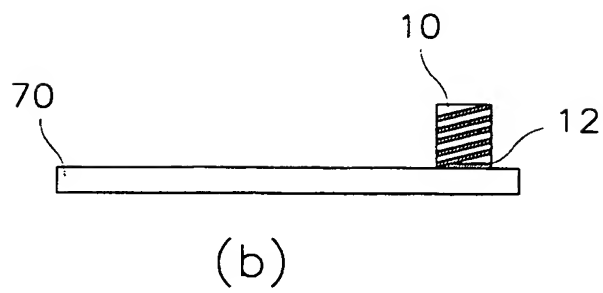
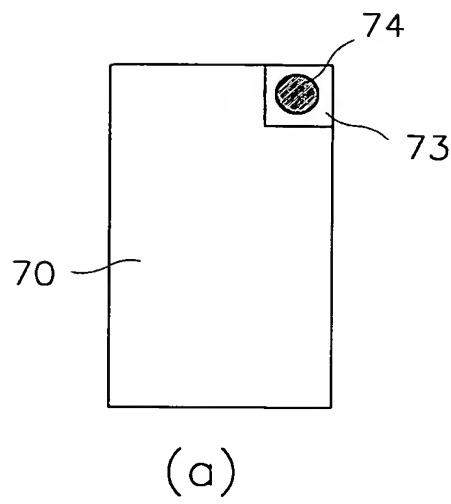
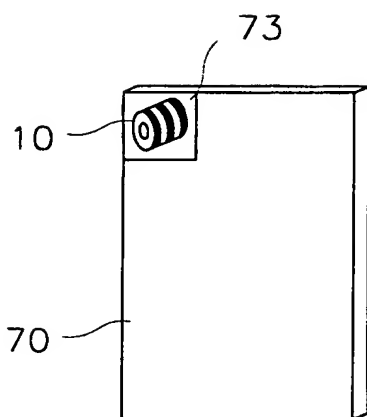
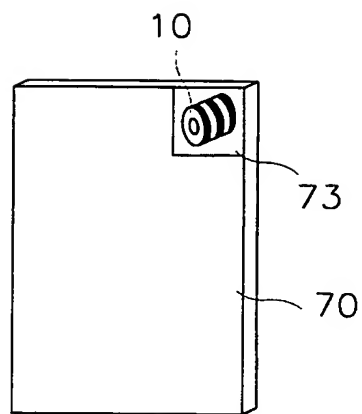


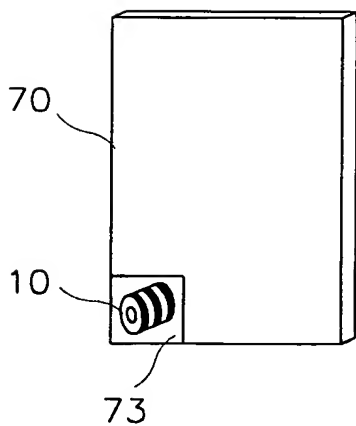
FIG. 17



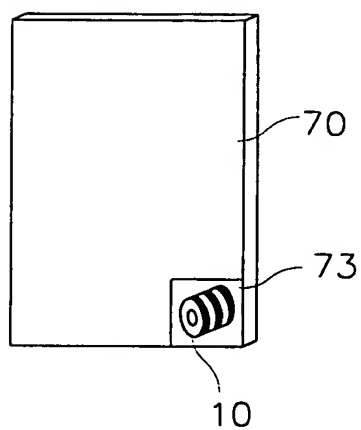
(a)



(b)



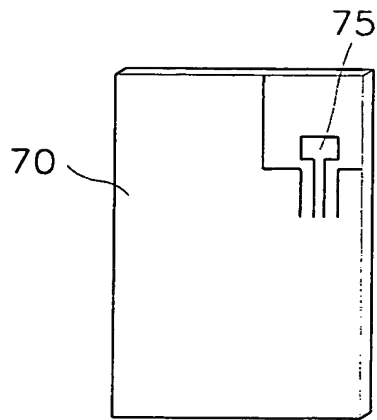
(c)



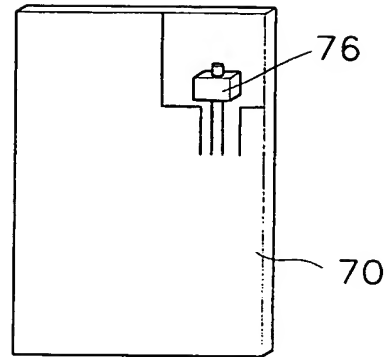
(d)

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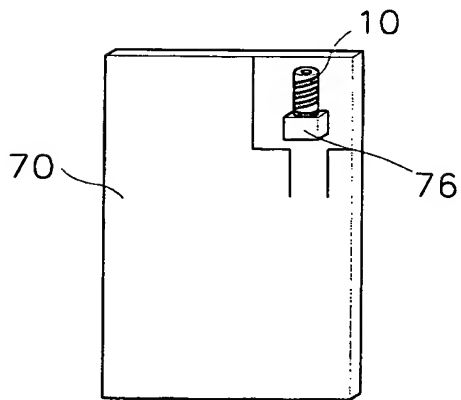
FIG. 18



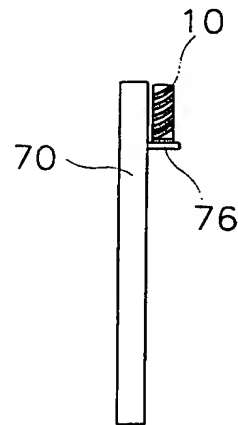
(a)



(b)



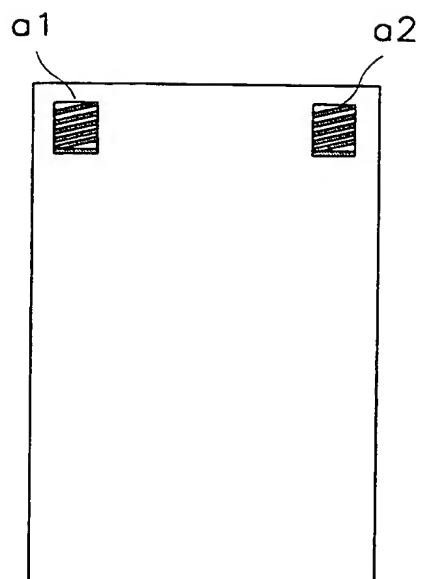
(c)



(d)

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FIG. 19



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FIG.20

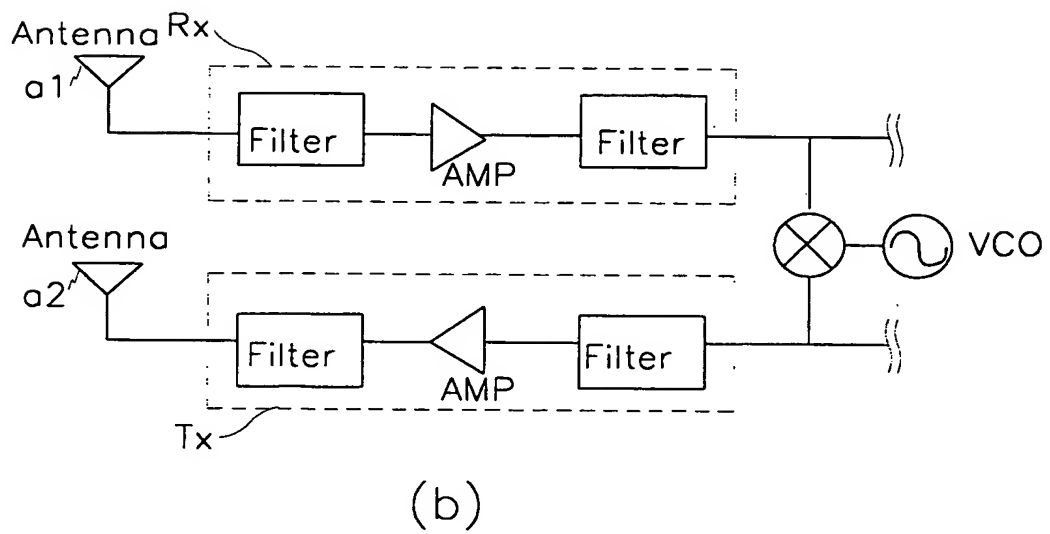
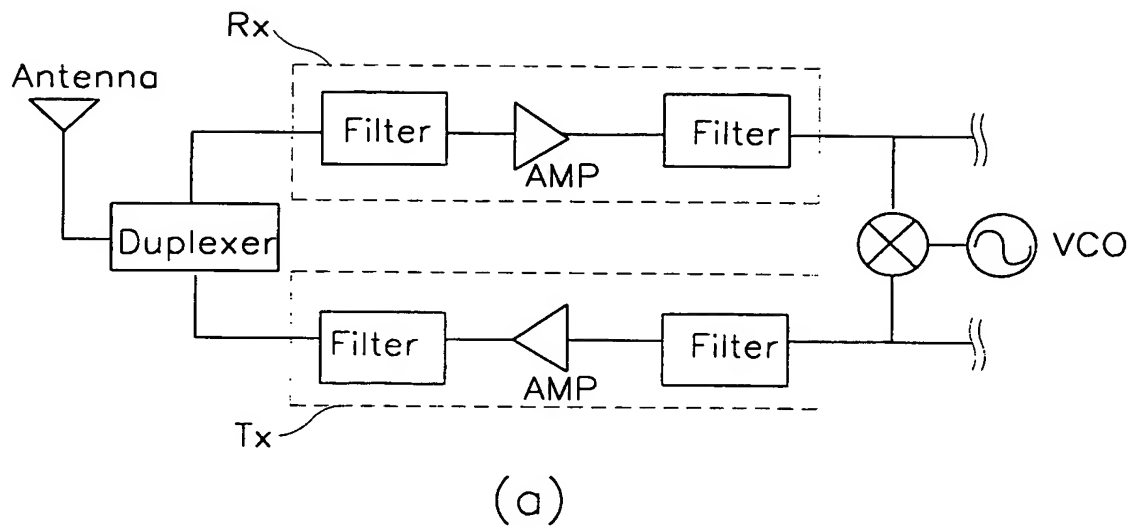
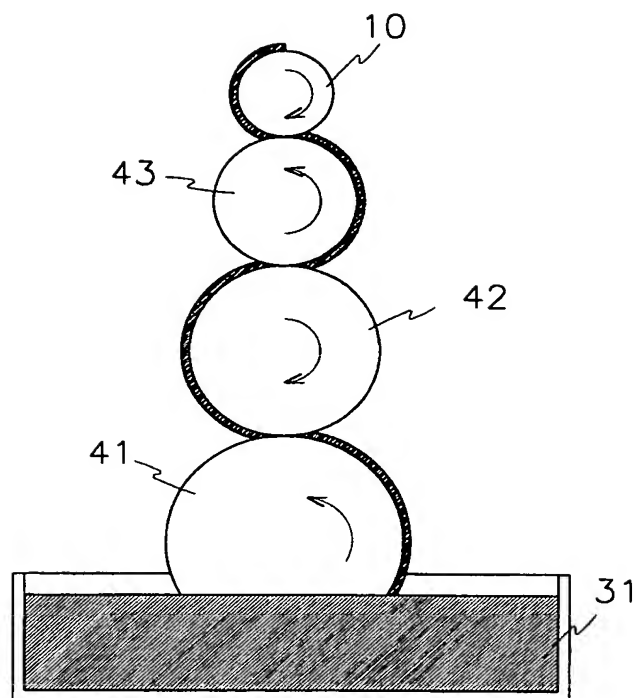
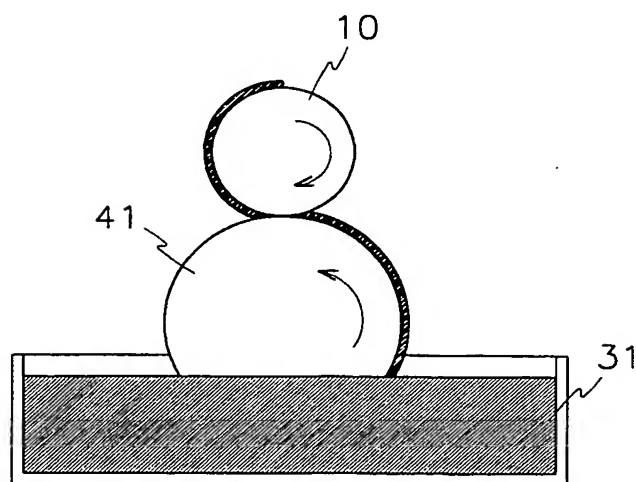


FIG.21



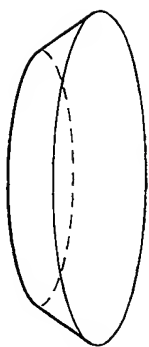
(a)



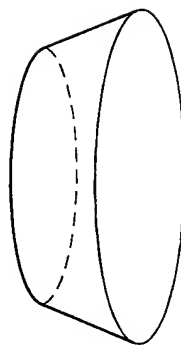
(b)

20080710 14:26:00

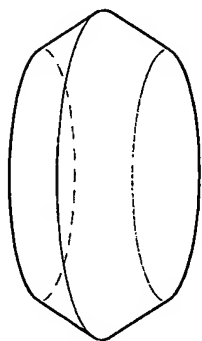
FIG.22



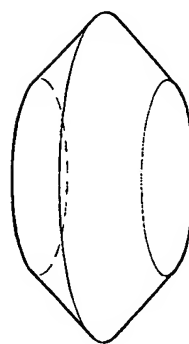
(a)



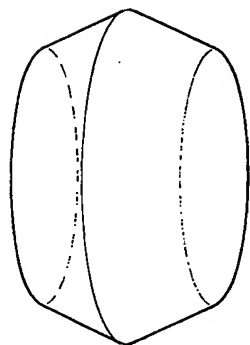
(b)



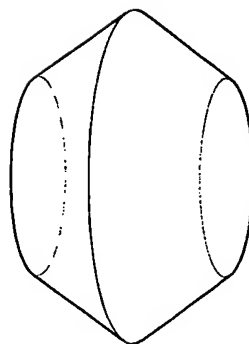
(c)



(d)



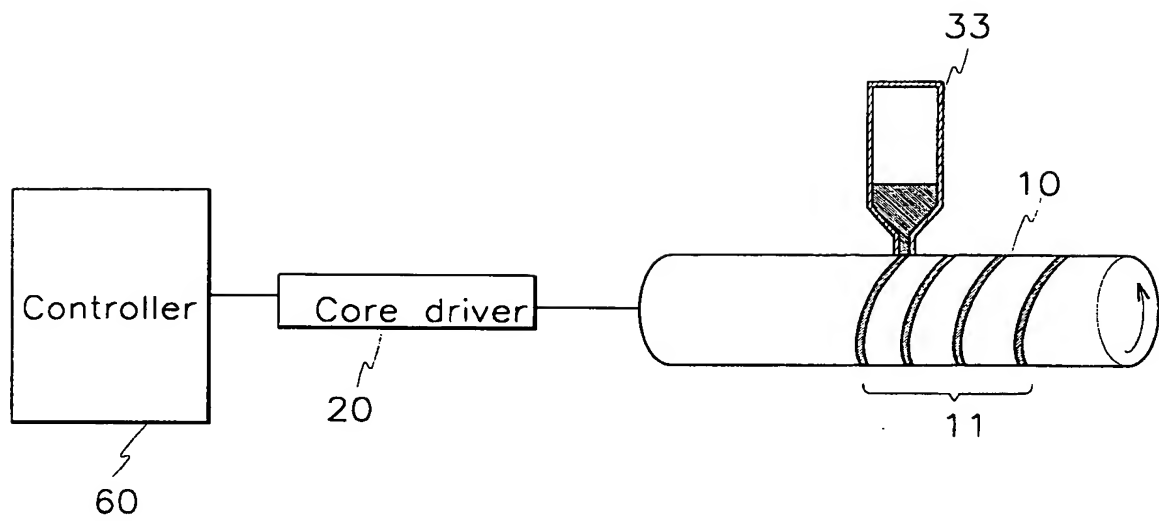
(e)



(f)

200070710-2260001

FIG.23



20091017 13:50:00

FIG.24

